AN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Seong Min Seo, Young Suk Chung, Jon Sik Paek, Jae Hun Ku, Jae Hak

Yee

Assignee:

Amkor Technology, Inc.

Title:

Semiconductor Package Including Stacked Chips

Serial No.:

09/816,599

Filing Date:

March 23, 2001

Examiner:

Not Yet Assigned

Group Art Unit:

Docket No.:

AB-1111 US

San Jose, California October 2, 2001

COMMISSIONER FOR PATENTS

Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT **UNDER 37 CFR § 1.97(b)**

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicants wish to call the documents listed on the accompanying PTO Form-1449 to the Examiner's attention. Copies of these documents are enclosed.

Citation of these documents shall not be construed as:

- an admission that the documents are necessarily prior art with respect to the 1. instant invention;
 - a representation that a search has been made, or 2.
- an admission that the information cited herein is, or is considered to be, 3. material to patentability as defined in § 1.56(b). RECEIVED

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Serial No. 09/816,599

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Applicants submit that no fee is required. If, however, a fee is necessary under 37 C.F.R. 1.97(c), then such fee and any other appropriate fee may be charged to Deposit Account No. 19-2386.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on October 2 2001.

Attorney for Applicants

Date of Signature

Respectfully submitted,

Kttorney for Applicants

Reg. No. 34,691

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